

Aspects in the Industry Standard AADL

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ABSTRACT

Aspect-Oriented Modeling is aimed at reducing the complexity of models by separating its different concerns. In model-based development of embedded systems this separation of concerns is more important given the multiple non-functional concerns addressed by embedded systems. These concerns can include timeliness, fault-tolerance, and security to name a few. The Architecture Analysis and Design Language (AADL) is a standard architecture description language to design and evaluate software architectures for embedded systems already in use by a number of organizations around the world. In this paper we discuss our current effort to extend the language to include new features for separation of concerns. These features not only include constructs to describe design choices but also routines to verify the proper combination of constructs from different concerns. This verification includes techniques and tools from the formal methods arena integrated into the AADL development tool providing a seamless design flow. We believe that work in this direction is fundamental to tackle the potential combinatorial explosion problem of verifying the merging of multiple concerns into a final system.

Categories and Subject Descriptors

D.2.2 [Software Engineering]: Design Tools and Techniques.

General Terms

Design, Standardization, Languages, Verification.

Keywords

ADL, AADL, Embedded, Separation of Concerns, SAE, Standard.

1. INTRODUCTION

The Architecture Analysis and Design Language (AADL) is a SAE (Society of Automotive Engineers) standard aimed at the

high level design and evaluation of the architecture of embedded systems [7]. Embedded systems are present in a wide spectrum of applications domains including avionics, automotives, signal processing, and industrial automation systems.

The different domains where embedded systems are present need to address different concerns such as safety, security, real-time response, and scalability among others. As a result, when these multiple concerns are described in an architecture description language (ADL) model, the complexity of such a model can grow very large. Furthermore, when one of the concerns of the model needs to be modified, manipulating the parts of the model related to that concern can prove to be a challenge since these parts are mixed with the elements from other concerns. Such a mixture can lead to unintended modifications difficult to prevent or even spot. Hence, language constructs to achieve a clean separation of concern are of paramount importance. In this paper we present the mechanisms that are currently being explored in AADL to achieve separation of concerns. Some of these mechanisms are an integral part of the current AADL standard, others are part of extensions for the second version of the standard and yet others are new proposals.

The work reported in this paper is part of the effort at the SEI to continue the enhancement of the AADL standard. This standard has been adopted or it is currently being explored by numerous commercial and research organizations. These organizations include AirBus Industries, NASA, the US Army, Toyota, Honeywell, Rockwell-Collins, UVa, LAAS, EWST, and UIUC.

1.1 Related Work

While most of the related work is cited throughout the paper, a couple of related papers are discussed here. These papers provide a general approach with some similarities to our own.

In [11], Iqbal and Elrad present an approach to model timing constraints as a cross-cutting concern. In their work they use real-time statecharts to model these constraints that are later woven into a final hierarchical timed automaton. Their view of timing is that of a collection of independent tasks with their own local clock. These clocks are synchronized with a global clock implemented by a central controller. This approach contrasts with our view of tasks as a flow of jobs to be performed with certain periodicity with no central controller (characteristic of the rate-monotonic [8] approach). In addition, we describe both the base application and the cross-cutting concerns with the same language (AADL). These descriptions are later combined using specialized statements.

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The spirit of the evaluation of design choices of AADL is also present in [10]. In this paper Reddy et al. present an approach to model aspects in UML along with descriptions of desirable and undesirable behaviors. These behaviors are modeled in sequence diagrams that are evaluated by integrating them to the final system and checking for the present of conflicts. While we agree on having a form of evaluating desirable behaviors we believe that because manual verification is error prone it may not yield practical improvements. In contrast AADL provides a framework for automatic verification of desirable properties. Furthermore, for the integration of the cross-cutting concerns into the base model we provide a way to encode assumptions and automatically verify them.

The rest of the paper is organized as follows. In Section 2 we provide a brief introduction to AADL. In Section 3 we present our new constructs for separation of concerns. In Section 4 we discuss the mechanisms we use to preserve the consistency in the integrated model. Finally, in Section 5 we present our conclusions.

2. INTRODUCTION TO AADL

AADL is an architecture description language developed to describe embedded systems. The purpose of a model in AADL is to describe the execution characteristics of the system. Because such characteristics depend on the hardware executing the software, an AADL model includes the description of both software and hardware.

In AADL systems are described with port-based components. The language has precise execution and communication semantics aimed at enabling simulation and analysis of execution characteristics of its models.

Components are described with types and implementations. Types define the elements of the components that are visible from the outside, e.g. ports. Implementations, on the other hand, describe subcomponents and connections. Such connections can be between the subcomponents ports as well as between the ports of the component and the ports of its subcomponents. Types and implementations can be extended (or subtyped) through inheritance in an OOP fashion. An extract of a sample banking system is presented in Listing 1.

```

system User
  features
    requestName: out event data port;
    name: in event data port;
end User;

system Account
  features
    input: in event data port;
    output: out event data port;
end Account;

system Banking
end Banking;

system implementation Banking.Impl
  subcomponents
    user: system User;
    account: system Account;
  connections
    requestName:
      user.requestName->account.input;

```

```

name:
  account.output->user.name;
end Banking.Impl;

```

Listing 1- Banking System

Listing 1 depicts both the type (*system Banking*) and the implementation (*system implementation Banking.Impl*) of a banking system. In the implementation the subcomponents *user* and *account* are defined. Two ports are defined in the types of each of the subcomponents and the connections of these ports are presented in the *Banking.Impl* implementation.

AADL has the core constructs to encode execution semantics according to the Generalized Rate-Monotonic Analysis (GRMA) theory [8]. For this purpose AADL uses specialized thread and process components with properties to encode their execution characteristics (according to GRMA).

Because embedded systems may need to comply with execution characteristics beyond timeliness (e.g. fault-tolerance), AADL provides extension mechanisms to accommodate new constructs and annotations.

2.1 Extension Mechanisms

AADL defines two main extension mechanisms: property sets and sublanguages (known as annexes).

Properties are label-value pairs used to annotate components. These properties can be grouped into named sets. These sets are then used in analysis tools that process AADL models to be able to verify characteristics of the modeled system.

One example of a property set is the standard property set for RMA that contains period, execution time, and deadline among other properties. These properties are associated with threads to be able to derive a real-time task set amenable of timing analysis.

Sublanguages, on the other hand, enable the encoding of complex statements about components for which syntactic verification makes sense. The syntax of the language is defined inside the annex that implements the language. In OSATE (our open source tool that implements the language), the annex is implemented as a plug-in to the tool. Just as with properties, appropriate analysis tools are used to analyze systems that include annex statements. Annex statements can be added inside a component or outside them packaged in annex sub-clauses (inside an AADL language clause). Annex sub-clauses are syntactic units of the sublanguage defined by its own rules.

One example of an annex is the Behavioral Annex. This annex is a standard annex¹ that encodes the behavior of components as state machines. A sample system with a behavioral annex sub-clause is depicted in Listing 2.

```

thread speed
  features
    tick: in event port;
    sp: out data port;
end speed;
thread implementation speed.impl
  annex behavioral_specification {**
    states
      s0: initial complete state;

```

¹ Annexes can become standard as part of the standardization process.

```

transitions
  s0-[]->s0 {sp:=tick'count;};
**};
end speed.impl;

```

Listing 2 - Behavioral Annex Example

Listing 2 depicts the type and implementation of a *speed* thread. The behavioral annex sub-clause shown inside the implementation exhibits a single state and a single transition. This transition outputs the tick counter received from the thread's input port into its output port.

Annexes and properties allow the addition of complex annotations to AADL models that accommodate the needs of multiple concerns. These annotations, along with their corresponding analysis plug-ins, provide a powerful combination for the architect to evaluate his/her design choices from different perspectives. The extension mechanisms in AADL enable these perspectives to evolve in number and complexity as the knowledge on them also evolves.

Even though the AADL extension mechanisms accommodate the evaluation of design decisions from multiple concerns it does not address the separation of these concerns. As a result, new aspect-like constructs and mechanisms are being explored to achieve this separation of concerns.

3. CONSTRUCTS FOR SEPARATION OF CONCERNS IN AADL

Separating parts of the model into different abstractions that can be manipulated separately implies that such abstractions do not interfere with each other. In AADL we use the functional model (the one that describes the logical transformations disregarding any non-functional properties) of a system as a base model and the constructs from other concerns as model transformations that merge a model segment (from other concern) into this base model. These model transformations are applied to the base to add non-functional behaviors such as redundancy for fault tolerance. Aspects in AOP can be seen in a similar fashion if we consider the weaving process as a transformation performed on a base program.

The main purpose of casting concerns descriptions as model transformation is to be able to apply them to a complete (from the functional point of view) base model without disassembling it (e.g. breaking connections between components). In other words, model transformations prevent manual manipulations of the model that lead to unintended behaviors (or the breaking of them).

3.1 Encoding Model Transformations

In AADL we have two model transformations: a plug-replacement and an interception. A plug-replacement transformation is aimed at adding a non-functional behavior confined to a functional component. The model segment used to add this behavior is named plug-replacement because it replaces a functional component that is plug-compatible. Plug-compatible in this case means that it shares the same interface (ports). Hence the replacement process is purely mechanical (and automated). Plug-replacements are created as templates that take as one of its parameter the component type that it is suppose to replace.

The interception, on the other hand, is aimed at adding the non-functional behavior to connections between components, e.g. encryption or caching. The model segment used to describe this

behavior is called a *coupler* because it defines the coupling between components in the functional model. The coupler concept has been borrowed from [2][3].

3.1.1 Interface to the Base Model

To interface model transformations to the base system we first build a full functional system and then we apply the transformations on it.

To illustrate this process we start with the system defined in Listing 1 from Section 2. Next, we define one transformation of each type. The plug replacement template (specified with the keyword *component*) takes as a parameter (in the *prototypes* section) the type of the component to be replaced. A sample definition for a redundant component is illustrated in Listing 3.

```

system Voter
  features
    input1: in event data port;
    input2: in event data port;
    input3: in event data port;
    output: out event data port;
end Voter;

component RedundantReplacement
  prototypes
    original: component;
  features
    input: in event data port;
    output: out event data port;
end RedundantReplacement;

component implementation RedundantReplacement.Impl
  subcomponents
    voter: system Voter;
    replica1: original;
    replica2: original;
    replica3: original;
  connections
    c1: input->replica1.input;
    c2: input->replica2.input;
    c3: input->replica3.input;
    c4: replica1.output->voter.input1;
    c5: replica2.output->voter.input2;
    c6: replica3.output->voter.input3;
    c7: voter.output->output;
end RedundantReplacement.Impl;

```

Listing 3 - Redundant Plug Replacement

The coupler on the other hand, can be defined as a regular system type. However, this system defines special ports that intercept connections of other systems. In particular, it defines an input port that intercepts the starting point of a connection, and an output port to intercept the output of the connection. A sample coupler is shown in Listing 4.

```

system Caching
  features
    srcRequest: in event data port;
    dstRequest: out event data port;
    srcResponse: in event data port;
    dstResponse: out event data port;
end Caching;

system implementation Caching.Impl;
  subcomponents
    findKey: system FindKey;
    saveKeyDataPair: system SaveKeyDataPair;
  connections
    c1: srcRequest->findKey.inKey;

```

```

c2: findKey.missingKey->dstRequest;
c3: srcResponse->
saveKeyDataPair.inKeyDataPair;
c4: saveKeyDataPair.data->dstResponse;
end Caching.Impl;

```

Listing 4 - Caching Coupler

In the example of Listing 4 the coupler *Caching*, defines two pairs of ports aimed at intercepting the *request* and *response* connections of a subsystem. Each pair defines an input and an output port to work as the interceptor of the input and the output of the connections respectively. In particular, the role of the *srcRequest* port is to receive the flow coming into a *request* connection (the particular name of the connection depends on the subsystem being intercepted). Conversely, the role of the *dstRequest* port is to provide the flow expected at the end of the connection once it is processed by the coupler.

The transformation is applied in a subtype of the functional system that includes the transformations as subcomponents. This is depicted in Listing 5.

```

system implementation FinalBanking.Impl extends
Banking.Impl;
subcomponents
  caching: system implementation Caching.Impl;
  redundantReplacement: system implementation
RedundantReplacement.Impl(original=>system
Account);
connections
  replace(account, redundantReplacement);
  intercept(requestName, caching.srcRequest,
caching.dstRequest);
  intercept(name, caching.srcResponse,
caching.dstResponse);
end FinalBanking.Impl;

```

Listing 5 - Application of the Transformations

In Listing 5 two new statements (and keywords) are introduced in the *connections* section: *replace* and *intercept*. Replace, as the name implies, replaces the subcomponent given in the first parameter by the one given in the second parameter. Intercept on the other hand intercepts the connection given in the first parameter with the ports given in the second and third parameters.

The order of the transformation is given by the order of appearance in the connections section to record the intention of the designer.

3.1.2 Recursive Transformations

Transformations can be applied recursively over other model transformations. In the case of plug-replacements another plug-replacement can replace one of its subcomponents. In the case of couplers, another coupler can intercept any connection inside the coupler or intercept connections in the base model multiple times in a protocol stack fashion. This stack routes the connection through the first coupler and then to the second and finally into the target of the intercepted connection. For instance, applying a caching coupler and then an encryption coupler to the same connection would first route the connection through the caching coupler and then to the encryption coupler. These recursive transformation patterns enable the creation of hierarchies in different concerns to better handle their complexity.

3.1.3 Transformations from Different Concerns

Model transformations can be addressing different concerns and hence be coming from different concern segments. The purpose of

separating these concerns is to be able to ignore any other concern when dealing with the concern at hand. The constructs presented here are aimed in that direction and enables the encoding of plug-replacements or couplers disregarding constructs from other concerns.

4. CONSISTENCY IN THE PRESENCE OF MULTIPLE TRANSFORMATIONS

The model transformations presented in this paper provide a good way to hide information not relevant for the concern at hand. The counter side of that property is that assumptions are made about the model being transformed. While these assumptions may be reasonable when a single transformation is applied, once a multiple transformations are applied over the same model the situation may change. In other words, the characteristics assumed from a model by one transformation may change if another transformation from another concern is applied on it. As a result, even though the transformations can be designed independently, its application over the model may need to be in a coordinated fashion to have a consistent final model.

We identify two types of consistency that need to be addressed: syntactic and semantic. The syntactic consistency is embedded in the topology of the model, while the semantic consistency depends on the designer's intents that go beyond what traditional languages structures can handle.

4.1 Syntactic Consistency

From the point of view of what can be encoded in the language, syntactic consistency can be reduced to the order of the transformations. We identify three main types of ordering namely: single concern order, orthogonal, and designer-defined order. In addition a couple of automatic reordering strategies are also being explored.

4.1.1 Single Concern Order

The transformation order from a single concern is part of the design space of the concern. As a result, it is fully a design decision to build the behavior of the semantics at hand. This ordering takes the form of recursive replacement or recursive interception.

4.1.2 Orthogonal

When two transformations are orthogonal the order in which they are applied is irrelevant. Ideally, all transformation from different concerns should be orthogonal. In practice only a subset of them really are.

4.1.3 Designer-Defined Order

When neither of the previous orders can be defined, the designer must define an order himself. In order to do this, a single point of decision must be reached. In AADL this is implemented as a derived component from the base functional system where the couplers and plug-replacements are listed and the transformation from the different concerns are applied in an explicit order as shown in Listing 5.

4.1.4 Automatic Reordering

We are also currently exploring the adoption of automatic reordering as presented in [3]. In this work two types of

reordering were implemented: Type-based reordering and instance-based reorder. In type-based reordering an order is established on the types of transformations. For instance, one may like to define that couplers that implement reliable channels (by resending upon lack of acknowledge) would be added after a couplers that implement encryption to avoid encrypting every time a message is resent. Based on this order, when both a reliable channel and an encryption transformation are applied an automatic reordering takes place ensuring that encryption happens always before reliable communication.

Instance-based reordering relies on the fact that some types of transformation can define orders for its own instances. One of the most common examples is ordering the locking and unlocking of mutexes, where a total order prevents deadlocks. As a result when a transformation that inserts mutex locking is applied, a total order in the locking is automatically ensured.

4.2 Semantic Consistency

Semantic consistency, as opposed to syntactic consistency, lacks support from the core AADL language. As in any programming language, the semantic consistency verification traditionally happens in the designers mind. This verification involves mentally matching the documentation of a module (e.g. function) against the required behavior needed when connecting it to another module (e.g. calling the function). When the composition flow (In [1] Bachmann et al. formalized this flow in a reasoning framework) between modules is unified (e.g. functional composition), this verification works well in practice. However, when modules from different compositions flows (different concerns) are combined together, the complexity of the semantic verification grows very quickly.

4.2.1 Transformation Assumptions

Model transformations constructs are built like any other part of a model or computer program. They are built with a specific application in mind and they are later on reuse in other applications. For these reasons, they have in general a set of assumptions that need to be observed. For instance, when trying to use a caching mechanism to reduce the traffic from a web-client application to the server, it is assumed that the answer that the application gets from a request to the server never changes. However, when reusing a caching pattern (encoded in a coupler for instance) if the designer is not aware of this assumption, he could reuse it in the wrong context (where the response varies from one request to the next) getting undesirable results very difficult to explain.

4.2.2 Encoding Assumptions

Assumptions of non-functional constructs (e.g. caching) have in general a temporal logic nature. In our group we are experimenting with Alloy [4], a relational logic language and tool where temporal logic statements can be built and checked in a concise way.

To be able to avoid getting into intractable verification problems we develop an Alloy dialect. This dialect models temporal logic assumptions at an abstract, yet verifiable, level. In addition, such a dialect is constrained to model computations that are both finite and bounded so a bounded verification can be performed [5].

The core of our Alloy dialect models port-based components that communicate tokens. Computations in this dialect are modeled as mapping from logic time to tokens in the component ports. Logic time is represented as a set of ticks.

Alloy models systems as a collection of sets and relations between sets. As a result, to model our dialect we define sets (called signatures in Alloy – keyword *sig*) for Ticks, Tokens, and Components. For each component one named relationship from ticks to tokens is built to represent ports. This can be seen in Listing 6.

```
sig Tick{}
sig Token{}
sig Component {
  port1: Tick -> one Token;
  port2: Tick-> one Token;
}
```

Listing 6 - Alloy Dialect Core

Note in Listing 6 that the port's relationship from ticks to tokens has the keyword *one* before token. This ensure that for each tick there is only one token in the port.

The Alloy dialect core shown in Listing 6 allows us to define non-functional assumptions to be automatically verified with Alloy. These assumptions are verified against guarantees that components can make (perhaps verified in the past when used in another system). Together the assumptions and guarantees build a contract framework [9] that can be verified at design time. This framework is called *Coupler Contracts*. An extract of a sample model that includes assumptions (encoded with the keyword *assumes*) and guarantees (encoded with the keyword *ensures*) for the cache coupler can be seen in Listing 7.

```
system Cache
  features
    reqSrc: in data port;
    reqDst: out data port;
    respSrc: in data port;
    respDst: out data port;
  end Cache;
system implementation Cache.Impl
  ...
  annex CouplerContracts {**
    assumes no t1,t2:tick{(reqDst[t1]==reqDst[t2] &
      respSrc[t1]!=respSrc[t2])}
  **};
end Cache.Impl;
system NameProvider
  features
    reqName: in data port;
    respName: out data port;
  end NameProvider;

system implementation NameProvider.Impl
  annex CouplerContracts {**
    ensures all t1,t2:tick
      {(reqName[t1]==reqName[t2] =>
        respName[t1]==respName[t2])}
  **};
end NameProvider.Impl;
```

Listing 7 - Extract of Coupler Contract Sample

The assumption from the cache coupler can be read as: There are no two instants in time when the same token is sent through the *reqDst* port and different tokens arrive in the *respSrc* port in those instants. On the other hand the *ensures* clause of the *NameProvider* component can be read as: For all two instants in time the fact that the token in the *reqName* port are the same in

both instants implies that the token in the *respName* port would be the same in both of those instants.

Coupler contract annex clauses are parsed by the OSATE tool, translated into our Alloy dialect and sent to the Alloy tool for verification. This translation involves translating *Assumes* clauses into *Assert* clauses and *Ensures* clauses into *Fact* clauses. In addition, for each assert clause a *check* Alloy statement is generated.

Alloy verifies models by adding instances to the sets defined by the model while trying to build counter examples for the assertions. To avoid infinite sets (and search) such an addition is limited by a scope parameter added to each set defined in the model in a check statement. In our dialect we automatically defined the scope for ticks and tokens to be the larger of the number of variables defined in the annex clauses and the ports involved in a comparison. With this strategy we reduce the scope to the minimum necessary to build a counter example and ensure a bounded search. An extract of the facts, assertion, and check statements in Alloy is presented in Listing 8.

```
fact NameProviderFact {
  all dst:NameProvider{all disj t1,t2:TICK{
    (dst.reqName[t1]=dst.reqName[t2] =>
     dst.respName[t1]=dst.respName[t2])
  }}
}
assert CacheAssert {
  all dst:NameProvider{no disj t1,t2:TICK{
    (dst.reqName[t1]=dst.reqName[t2] and
     dst.respName[t1]!=dst.respName[t2])
  }}
}
check CacheAssert for 1 NameRequester,
  1 RequestingName,1 NameProvider,1 Cache,
  2 TICK,2 TOKEN
```

Listing 8 - Extract of Alloy Fact, Asserts, and Checks

It is worth noting that the absence of the fact *NameProviderFact* would cause Alloy to generate a counter example signaling the existence of a problem. This verification mode of identifying the lack of guarantees provides a nice design flow where the designer is forced to verify all assumptions.

5. CONCLUSIONS

In this paper we presented the constructs and mechanisms we are currently exploring in AADL to support separation of concerns. We showed how the proposed constructs are paired with merging mechanisms aimed at keeping the consistency of the final model. These mechanisms address both syntactic and semantic consistency.

We believe that the semantic verification of aspect-oriented modeling tools and languages is a core problem to address. Such verification requires novel approaches to encode the semantics of the different aspects of models and their coherent integration. We showed how theorem proving techniques provide an interesting approach for richer, yet concise, ways to encode and verify such semantics.

While the focus of this paper was to present the general approach to separation of concerns in AADL, we hope to report on individual mechanisms in the future.

6. REFERENCES

- [1] Bachmann, F. Bass, L., Klein, M., Shelton, C. Designing Software Architectures to Achieve Quality Attribute Requirements. Software, IEE Proceedings. Vol 152, Issue 4. August 2005.
- [2] de Niz, D., Bhatia, G. and Rajkumar, R. Model-Based Development of Embedded Systems: The SysWeaver Approach. IEEE Real-Time and Embedded Systems and Applications Symposium. August 2006.
- [3] de Niz, D. Modeling Functional and Para-Functional Concerns in Embedded Real-Time Systems. Ph.D. Thesis, Carnegie Mellon University, Pittsburgh, PA, 2004.
- [4] Jackson, D. Alloy 3.0 Reference Manual. May 2004.
- [5] Clarke, E., Biere, A., Raimi, R., and Zhu, Y. Bounded Model Checking Using Satisfiability Solving. Formal Methods in System Design. Vol 19, Issue 1, July 2001. Kluwer Academic Publishers.
- [6] Feiler, H. P., Lewis, B., Vestal, S. The SAE Architecture Analysis and Design Language (AADL) Standard. IEEE RTAS Workshop, 2003.
- [7] SAE-AS5506. SAE Architecture Analysis and Design Language (AADL). International Society of Automotive Engineers, Warrendale, USA, November 2004.
- [8] Klein, M., Ralya, T., Pollak, B., Obenza, R., Gonzalez Harbour, M. A Practitioner's Handbook for Real-Time Analysis: Guide to Rate Monotonic Analysis for Real-Time Systems. 1993. Springer.
- [9] Meyer, B. Applying Design by Contract. IEEE Computer. Vol 25, No 10. October 1992.
- [10] Reddy, R., France, R., and Georg, G. An Aspect Oriented Approach to Analyzing Dependability Features. AOM 2005.
- [11] Iqbal, A., Elrad, T. Modeling Timing Constraints of Real-Time Systems as Crosscutting Concerns. AOM 2006.